# An Xu

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## **Employment**

Jan 2025 - now Applied Scientist at Amazon, Seattle, WA

Foundation model training for Shopping LLM.

Jan 2025 - Jan 2025 Senior Research Scientist at ByteDance/TikTok, Bellevue, WA

Jun 2023 - Jan 2025 Research Scientist at ByteDance/TikTok, Bellevue, WA

LLM distributed training and inference efficiency. Code generation.

## **Internship**

Jun 2022 - Aug 2022 Applied Scientist Intern at AWS AI, Santa Clara, CA

Large-batch optimization for LLM pretraining.

## Jun 2021 - Aug 2021 Applied Research Intern at NVIDIA Research, Remote

Propose new cross-silo federated learning methods (CVPR 2022, ECCV 2022), which are publicly available in NVIDIA's NVFLARE link1 and link2 respectively.

#### Jun 2019 - Nov 2019 Research Intern at JD Digits AI Lab, Mountain View, CA

Accelerate deep learning model parallelism by incorporating staleness (pipeline parallelism) (CVPR 2020).

## **Education**

### Aug 2018 - Aug 2023 University of Pittsburgh, Pittsburgh, PA

Ph.D in Electrical and Computer Engineering.

## Sep 2013 - Jul 2017 Tsinghua University, Beijing

B.Eng in Electrical Engineering. Second degree: B.Eng in Business Administration.

### **Publications**

[Homepage/Publications][Google Scholar]

16 publications with 500+ citations in top machine learning conferences covering Sparse MoE LLM pruning, Large-scale Distributed Training (Data and Model Parallelism) and Federated Learning, etc.

Served as program committee / reviewer for:

- ICLR'22-25; ICML'22-24; NeurIPS'22-24. ICML'22 session chair and outstanding reviewer.
- CVPR'21-23; ICCV'21, 23; ECCV'20, 22, 24
- AAAI'22-25; IJCAI'23
- KDD'20, 23; CIKM'22
- Journals: TNNLS, TMI